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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Proell. et al. Docket No.: INF-120
Serial No.: 10/733,332 Art Unit: 2818
Filed: December 12, 2003 Examiner: TBD
For: Integrated DRAM Semiconductor Memory and Method for Operating the Same

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: January 6, 2005

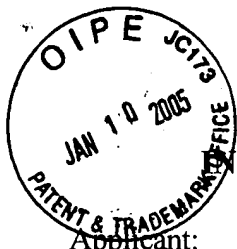
I hereby certify that the below listed correspondence is being deposited with the United States Postal Service on the date indicated above as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Certificate of Mailing via First Class Mail (1 page)
Information Disclosure Statement (1 page)
IDS Form PTO/SB/08a (1 page) citing (3) references
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Respectfully submitted,

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Mail Stop: Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08a that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

Respectfully submitted,

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January 6, 2005

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